

FIG. 1

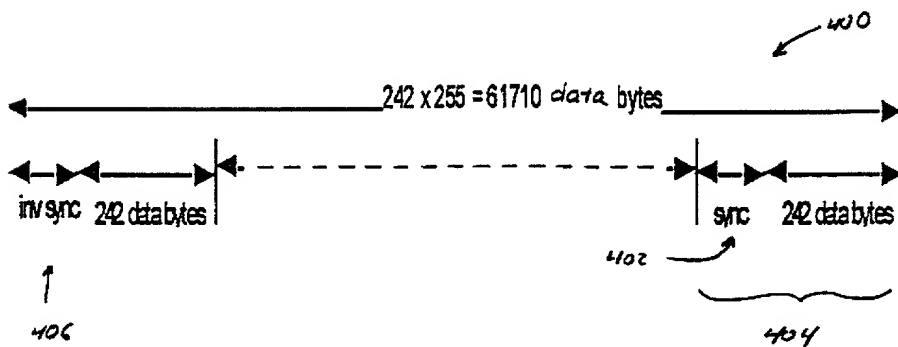


FIG. 4

FIG. 2

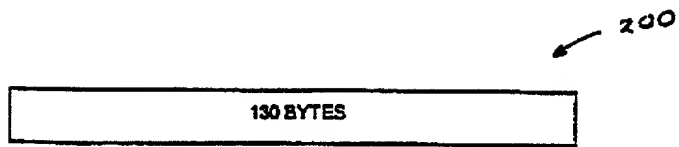


FIG. 2

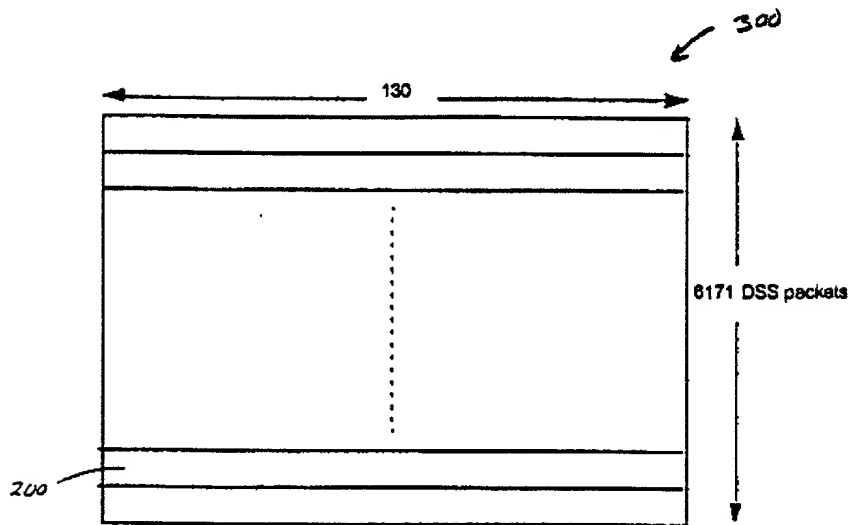


FIG. 3

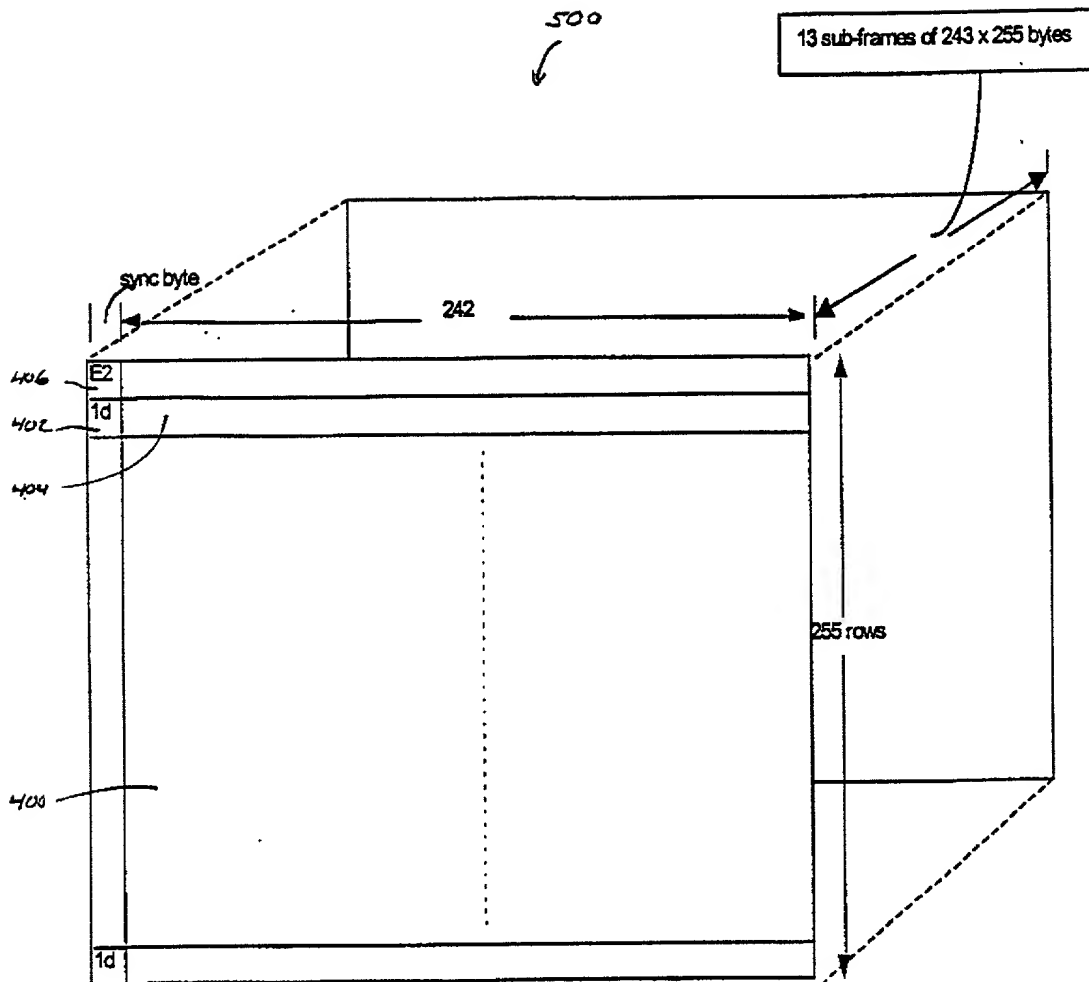


FIG. 5

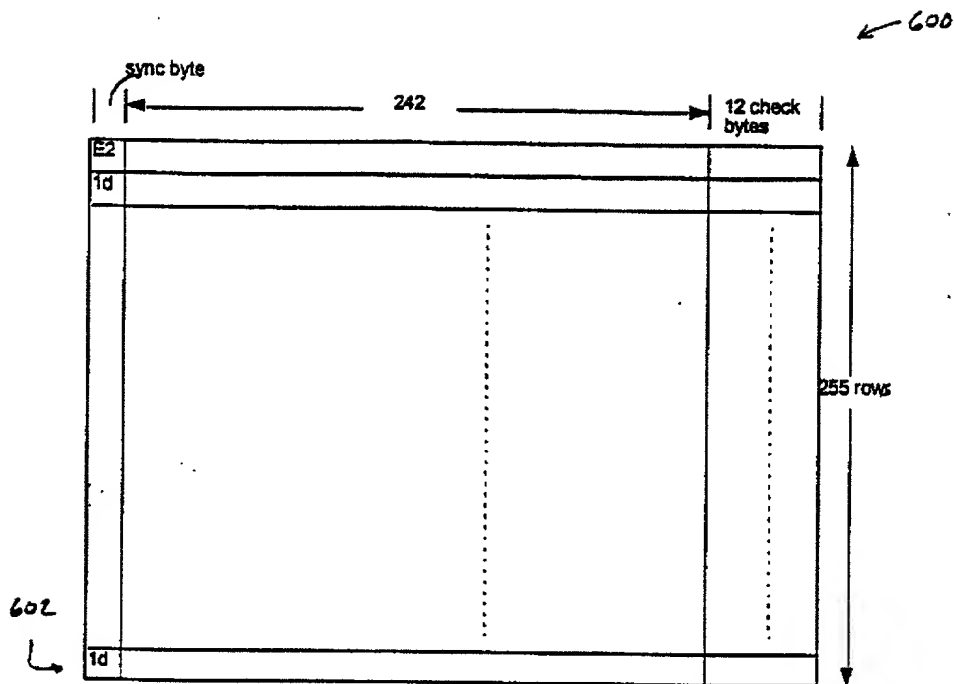


FIG. 6

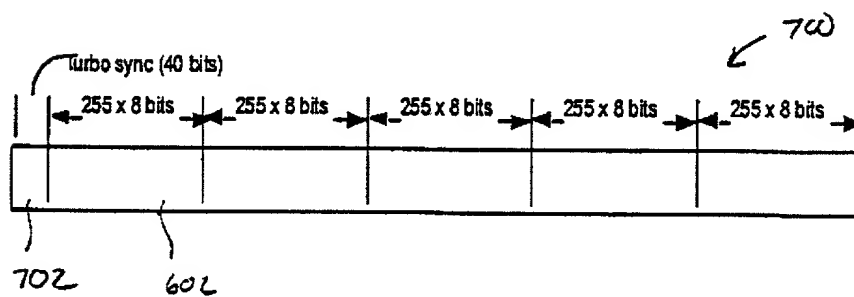


FIG. 7

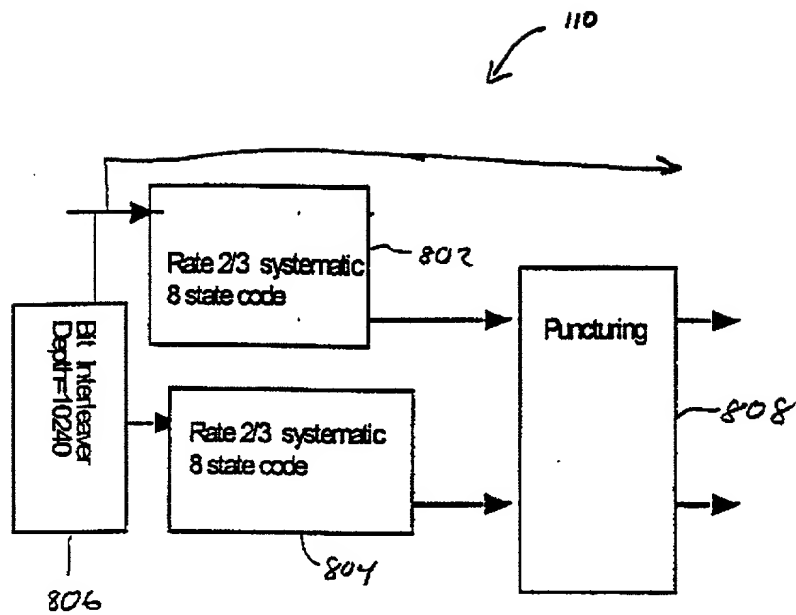


FIG. 8

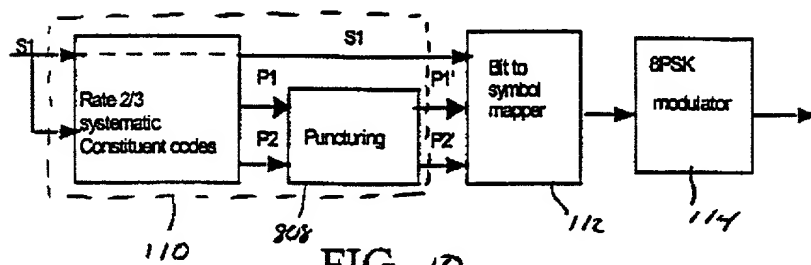


FIG. 10

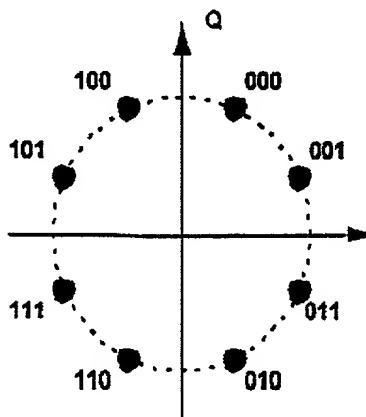


FIG. 11

FIG. 9a

$\xrightarrow{S1}$	(2 bits)	(2 bits)
$\xrightarrow{P1}$	1	X
$\xrightarrow{P2}$	X	1

FIG. 9b

$\xrightarrow{S1}$	(2 bits)	(2 bits)	(2 bits)	(2 bits)	(2 bits)
$\xrightarrow{P1}$	1	X	X	X	X
$\xrightarrow{P2}$	X	1	X	X	X

FIG. 9c

$\xrightarrow{S1}$	(2 bits)	(2 bits)	(2 bits)	(2 bits)	(2 bits)	(2 bits)	(2 bits)	(2 bits)
$\xrightarrow{P1}$	1	X	X	X	X	X	X	X
$\xrightarrow{P2}$	X	1	X	X	X	X	X	X

FIG. 9d

$\xrightarrow{S1}$	(2 bits)	(2 bits)	(2 bits)	(2 bits)	(2 bits)	(2 bits)	(2 bits)	(2 bits)
$\xrightarrow{P1}$	1	X	X	X	X	X	X	X
$\xrightarrow{P2}$	1	X	X	X	X	X	X	X

FIG. 9e

$\xrightarrow{S1}$	1	1	1	1
$\xrightarrow{P1}$	1	X	X	X
$\xrightarrow{P2}$	X	X	1	X

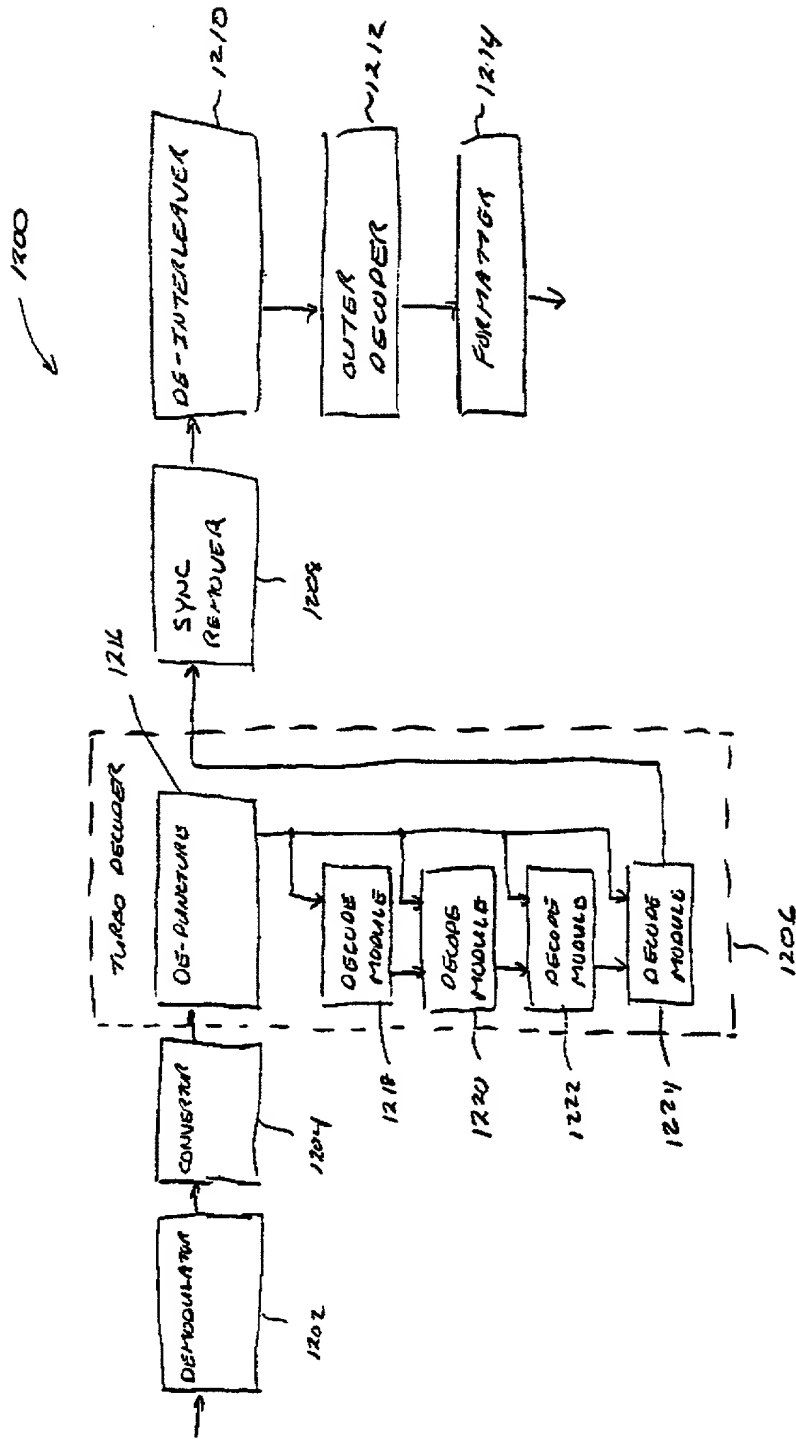


FIG. 12

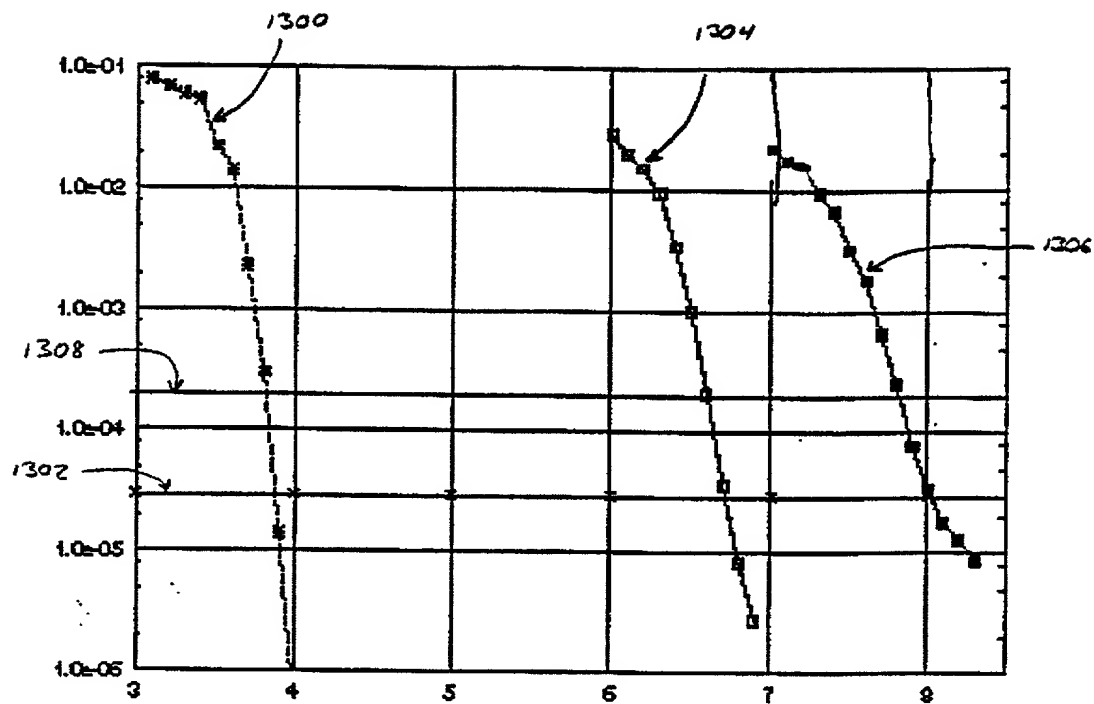


FIG. 13